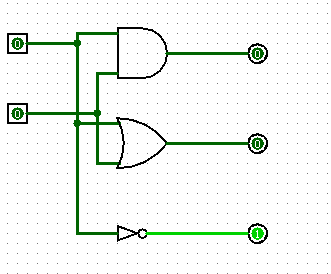
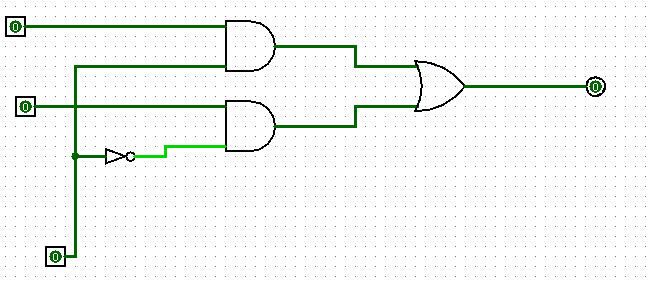
**Circuit Designs CSIS 2810**

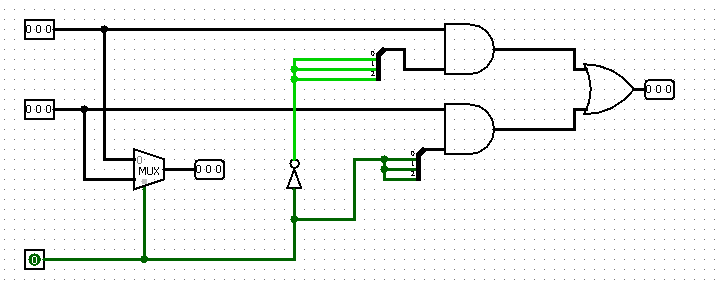
**Basic Gates: And, Or Not**

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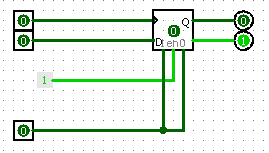
**1 bit MUX(Multiplexer)**

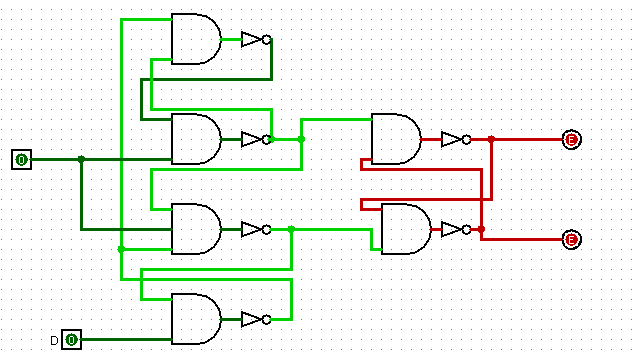
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**3 bit MUX (Gates and MUX part; also uses a splitter and multi-bit IO)**

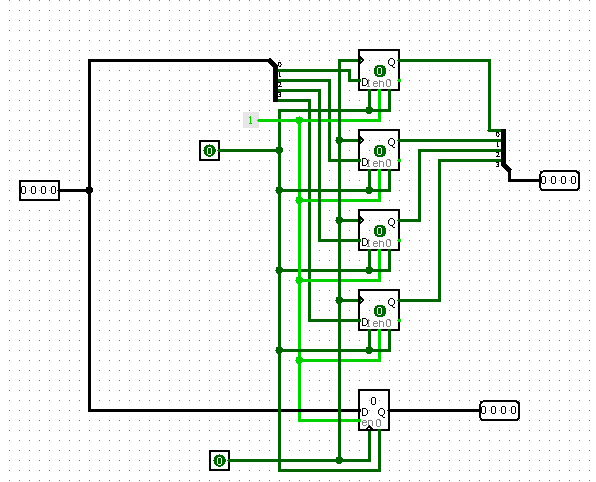
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**D Flip Flop(Gates and D FlipFlop part, uses a constant)**

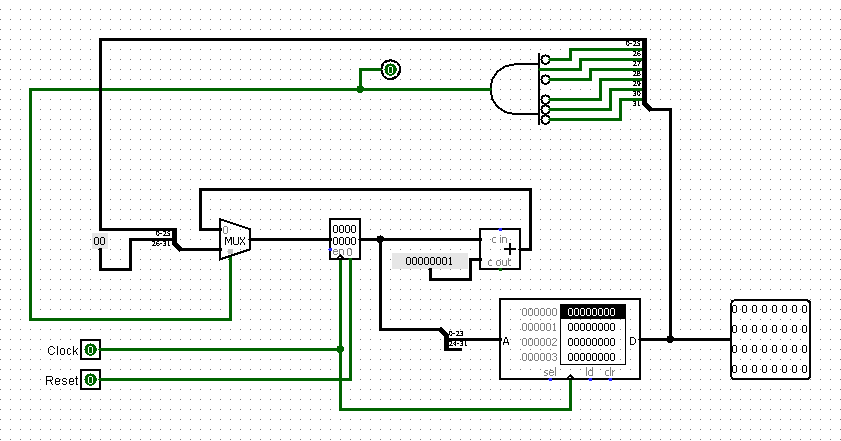
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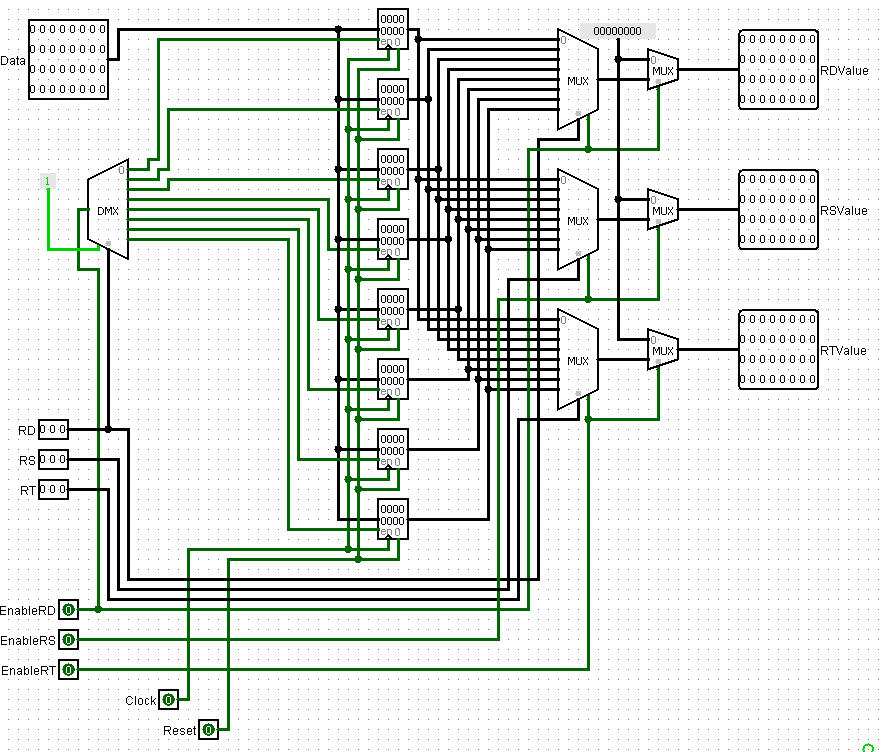
**4 Bit Register(Gates and Part)**

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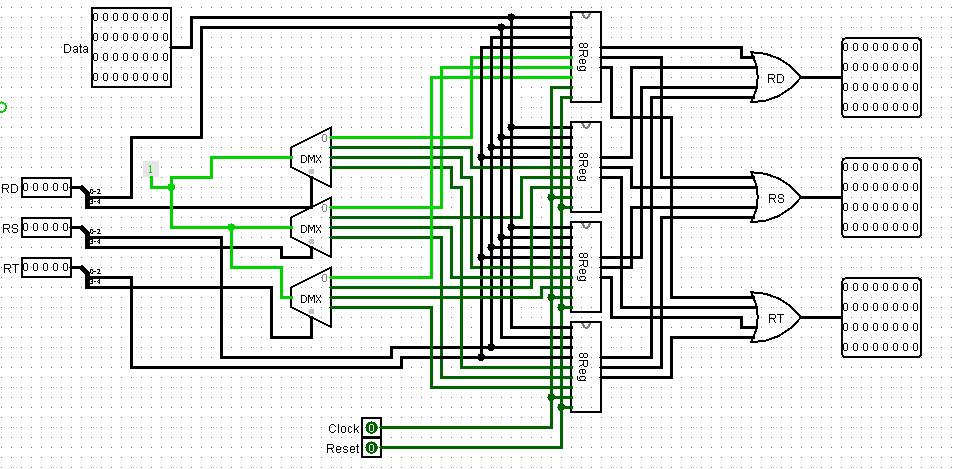
**PC(Program Counter, uses memory)**

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**RF Subpart(Register File Subpart)**

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**RF(Register File)**

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